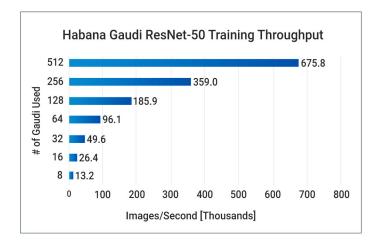


The Gaudi™ HL-2000 is an advanced AI and Deep Learning Training processor, leveraging purpose-built architecture and delivering superior performance, scalability, power efficiency and cost savings. The Habana HL-205 mezzanine card, and the HL-20X PCIe card both incorporate a single Gaudi HL-2000 Processor that contains a cluster of eight fully programmable Tensor Processing Cores (TPC™2.0). The TPC core is C-programmable, providing the user with maximum flexibility to innovate. The HL-205 is compliant with the OCP-OAM (open compute Accelerator mezzanine) specification.

The Gaudi is designed to accelerate various Al Training workloads such as image recognition, neural machine translation, sentiment analysis, recommender systems and many others. The Gaudi HL-2000 includes native integration of RoCE v2 RDMA functionality delivering two Terabits-per-second over 10 x 100GbE or 20 x 50GbE for inter-Gaudi communication by direct routing or via standard Ethernet switching. A single Gaudi delivers training throughput of 1,650 images-per-second on the ResNet-50 benchmark while consuming 140 Watts.





	HL-205 HL-200/202 Mezzanine Card PCle Card
PROCESSOR TECHNOLOGY	Gaudi HL-2000
HOST INTERFACE	PCle Gen 4.0 X 16
MEMORY	32GB HBM2
MEMORY BANDWIDTH	1TB/s
ECC PROTECTED	Yes
MAX. POWER CONSUMPTION	300W 200W
SCALE-OUT INTERCONNECT	RDMA (RoCE v2) 10x100Gbps 8x100Gbps or 20x50Gbps 0r 16x50Gbps Dual QSFP-DD ports
FORM FACTOR AND SKUS	OCP Accelerator Module Compliant Full Height/Length HL-200 HL-202 Passive Active Cooling Cooling

Technology Innovation

Gaudi introduces a unique combination of technology innovations, as a high-performance and fully programable AI processor with high memory bandwidth/capacity and scale-out based on standard Ethernet technology. With its wide array of connectivity options, Gaudi enables system integrators to build training systems of any scale, from low-cost servers to complete racks using a variety of Ethernet switches and scale-out topologies, all while using the same standards-based, scale-out technology.



Compute Architecture

Based on the proven, shipping Goya architecture, Gaudi leverages Habana's fully programmable TPC and GEMM Engine, supporting the most advanced data types for Al: BF16 and FP32. The TPC core was designed to support Deep Learning training and inference workloads. It is a VLIW SIMD vector processor with instruction set and hardware that were tailored to serve these workloads efficiently.



Memory bandwidth and capacity are as important as compute capability. Gaudi incorporates the most advance HBM memory technology, supporting extremely high memory capacity of 32GB and total throughput of 1TB/s. Gaudi's cutting-edge HBM controller is optimized for both random access and linear access, providing record-breaking throughput in all access patterns.



Scale Out Integrated RDMA

Gaudi is the only AI training processor to integrate On-Chip RDMA (RoCE v2) and interface directly with mature and widely used Ethernet networking. The HL-2000 chip interconnect technology is based on 20 pairs of 56Gbps Tx/Rx PAM4 SerDes that can be configured as 10 ports of 100Gb Ethernet or 20 ports of 50GbE/25GbE, or any combination of both.

SynapseAI™ and Software Development Tools

The Gaudi is supported by the SynapseAI™ software stack and advanced development tools. SynapseAI is Habana's custom-developed compiler and runtime. In its training incarnation, it is built for seamless integration with existing frameworks that both define a Neural Network for execution and manage the execution Runtime. SynapseAI can be interfaced directly using either C or Python API. By supporting DNN frameworks, SynapseAI enables users to unleash the power of Deep Learning by executing the algorithms efficiently using its high-level software abstraction. SynapseAI supports both Imperative/Eager mode and Graph mode.



For more details on Gaudi's performance and scaling, see our Whitepaper.

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